

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Tetsuro ASANO *et al.*

Serial No.: 10/521,941

Filing Date: February 6, 2006

For: SEMICONDUCTOR DEVICE

Examiner: Jerome Jackson Jr.

Group Art Unit: 2815

Confirmation No.: 2214

**PRELIMINARY AMENDMENT**

MS RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to the next examination of this application in consequence of the Request for Continued Examination filed concurrently herewith, please amend this application as follows:

**The listing of claims begins on page 2.**

**Remarks begin on page 11.**